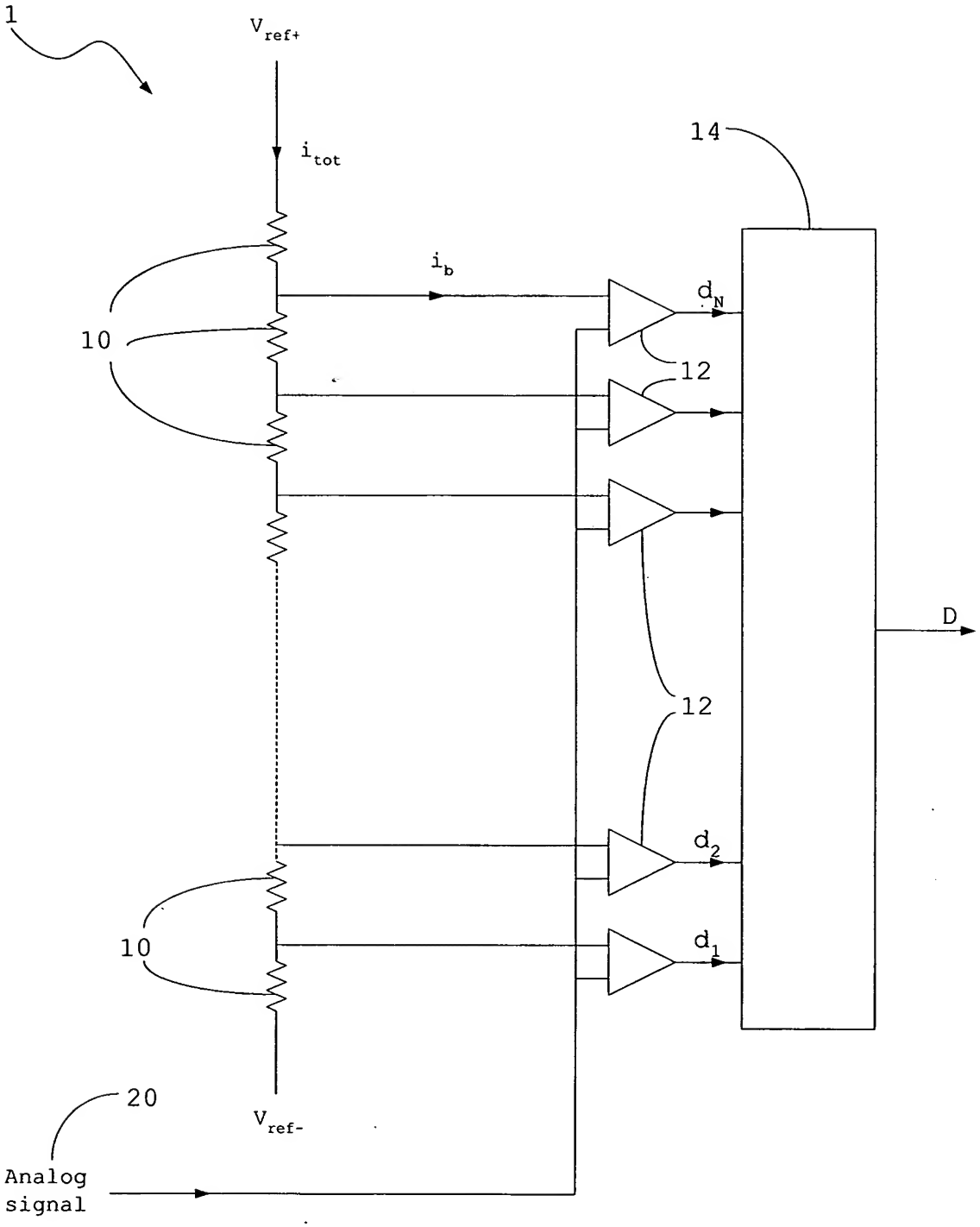

FIGURE 1 (PRIOR ART)

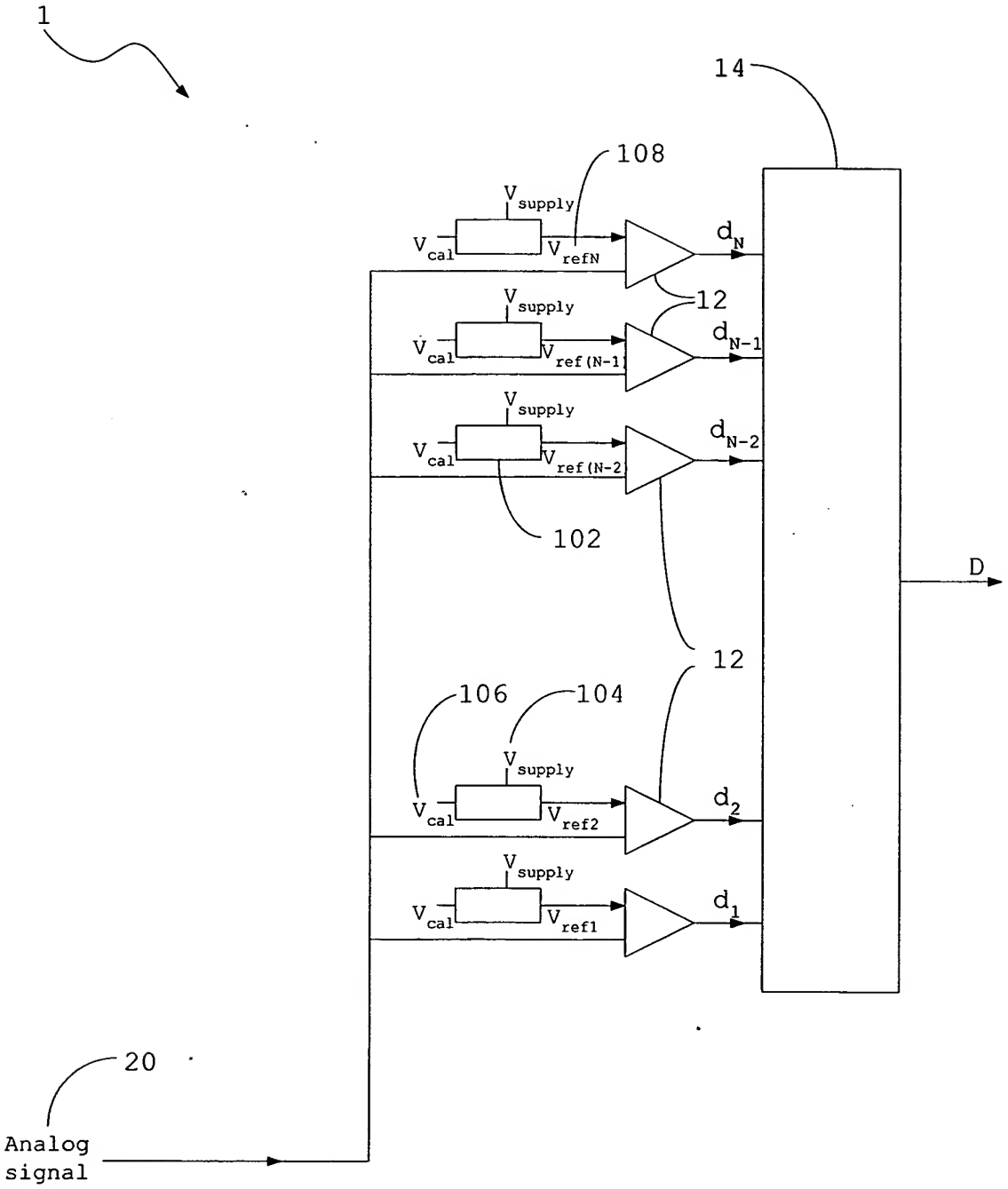
1
14
108
V_{supply}
V_{cal}
V_{refN}
d_N
12
$V_{ref(N-1)}$
d_{N-1}
$V_{ref(N-2)}$
d_{N-2}
102
D
106
104
V_{ref2}
d_2
V_{ref1}
d_1
20
Analog signal

FIGURE 2

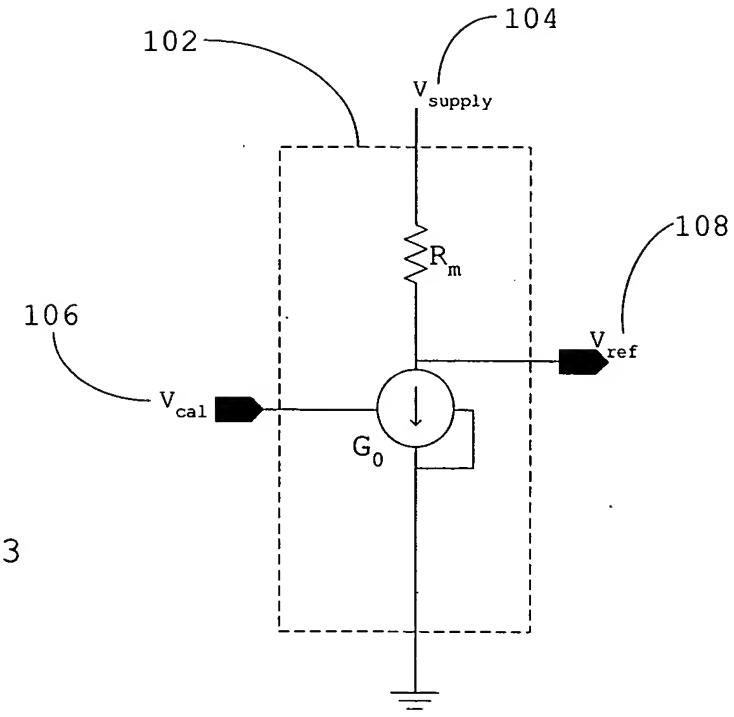

FIGURE 3

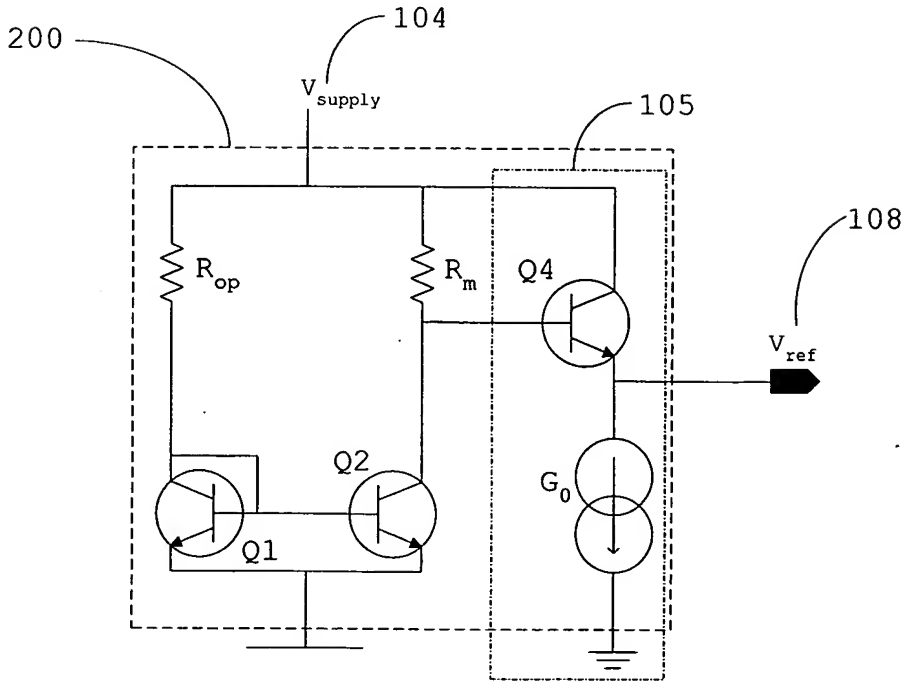

FIGURE 4

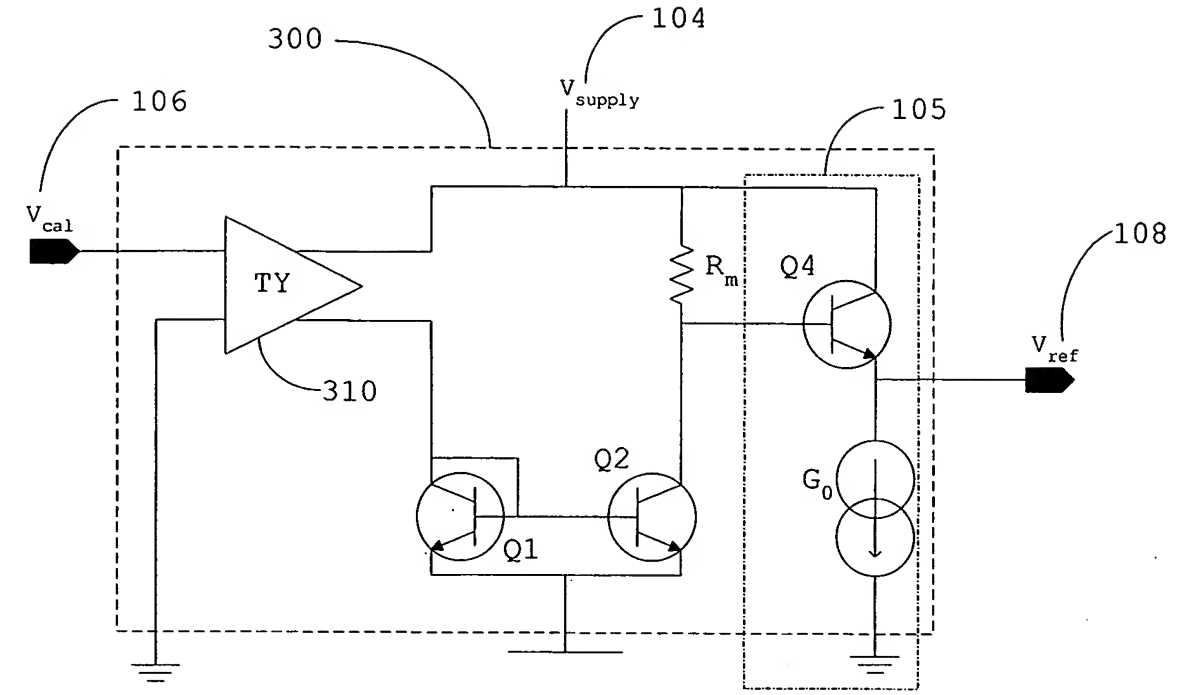

FIGURE 5

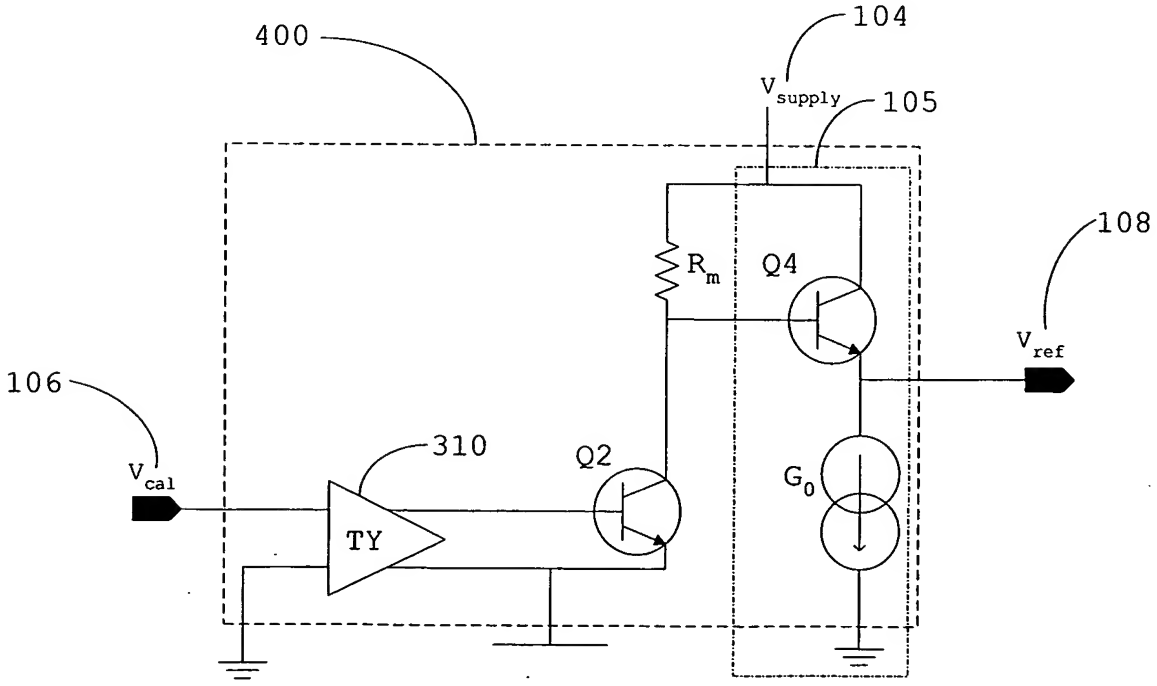

FIGURE 6

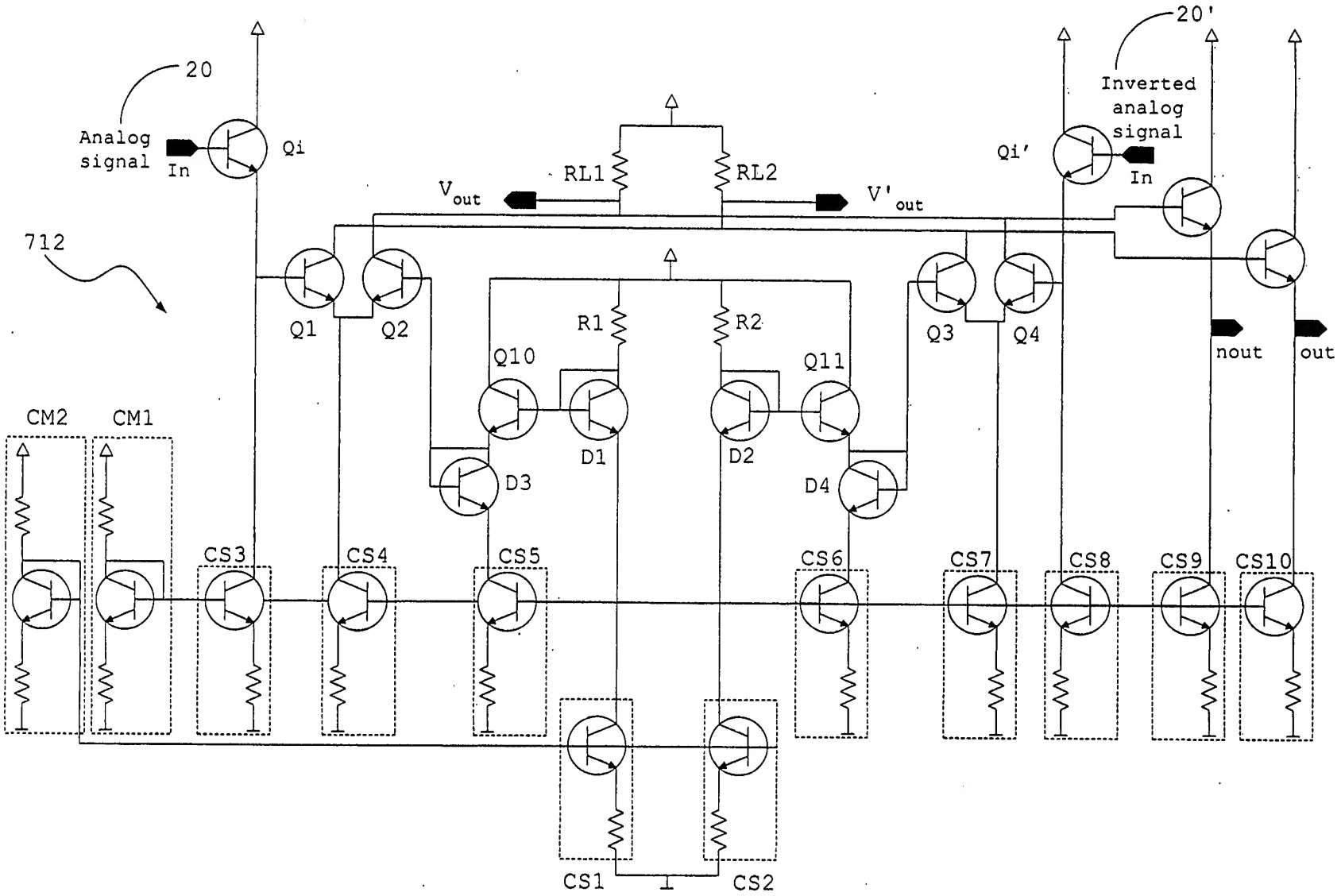
20
Analog signal
In
Qi
712
20'
Inverted analog signal
Qi'
In
RL1
RL2
V_{out}
V'_{out}
Q1
Q2
Q3
Q4
R1
R2
Q10
Q11
D1
D2
D3
D4
nout
out
CM2
CM1
CS3
CS4
CS5
CS6
CS7
CS8
CS9
CS10
CS1
CS2

FIGURE 7

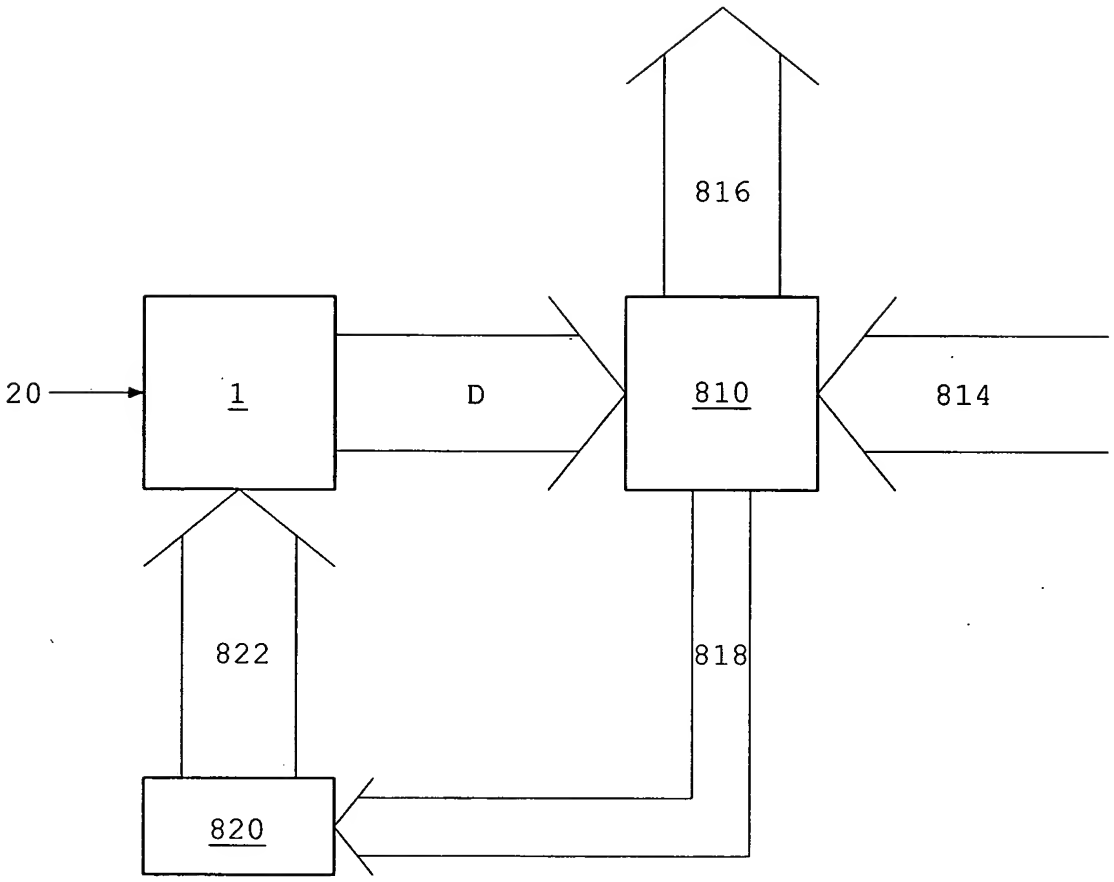

FIGURE 8